

Figure 1
PRIOR ART

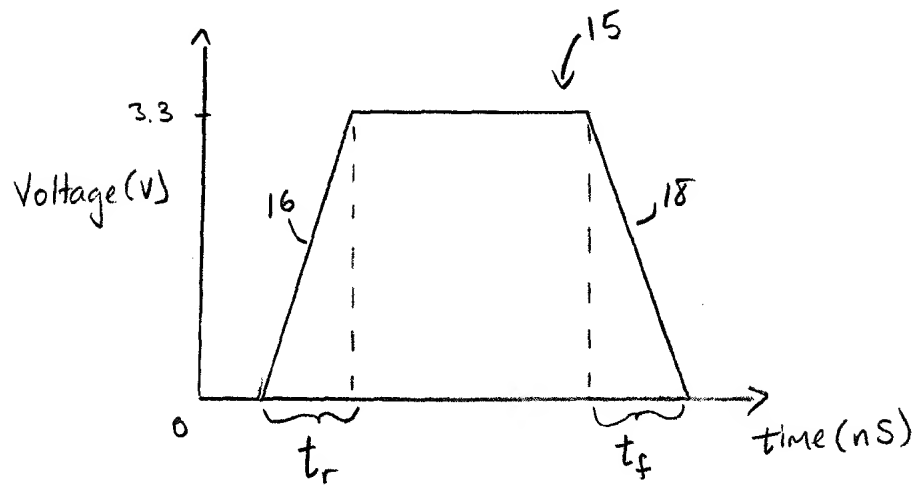


Figure 2A

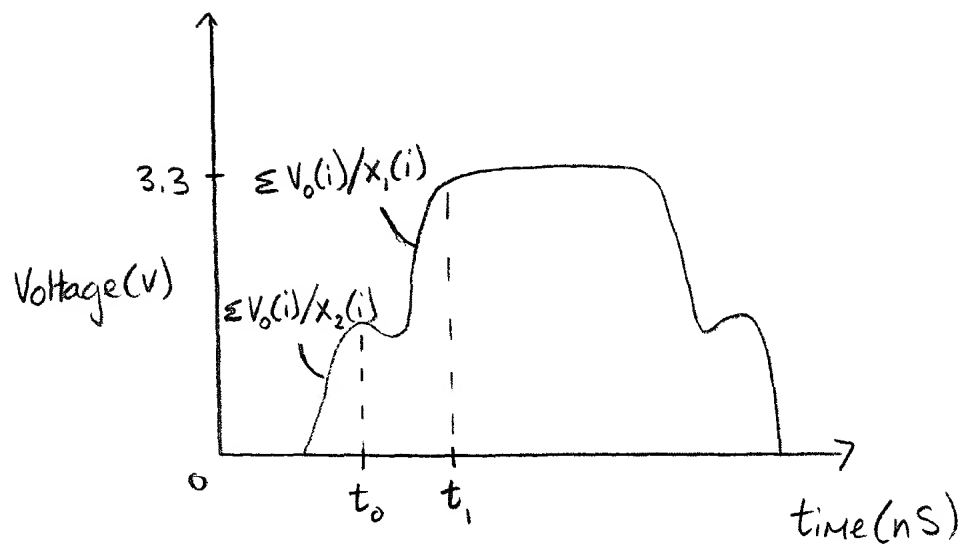


Figure 3

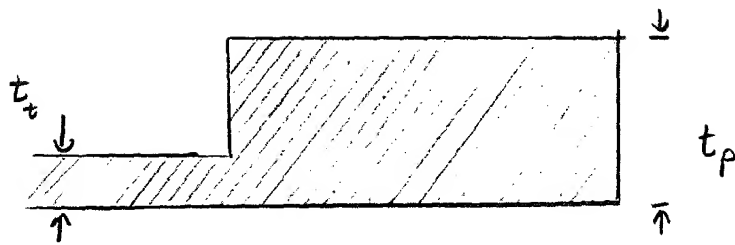


Figure 4A

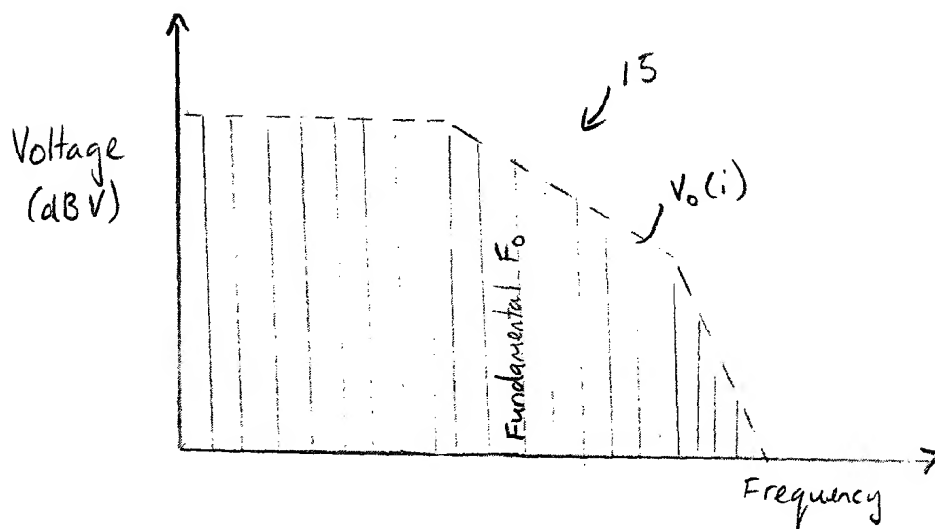


Figure 2B

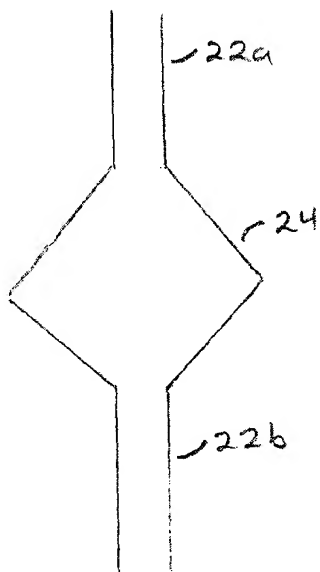


Figure 6

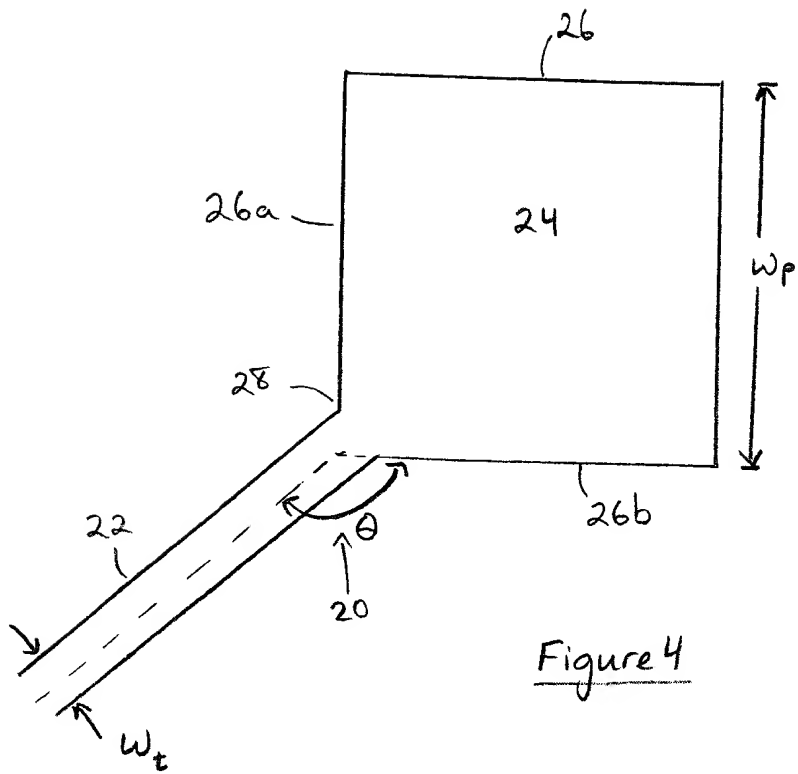


Figure 4

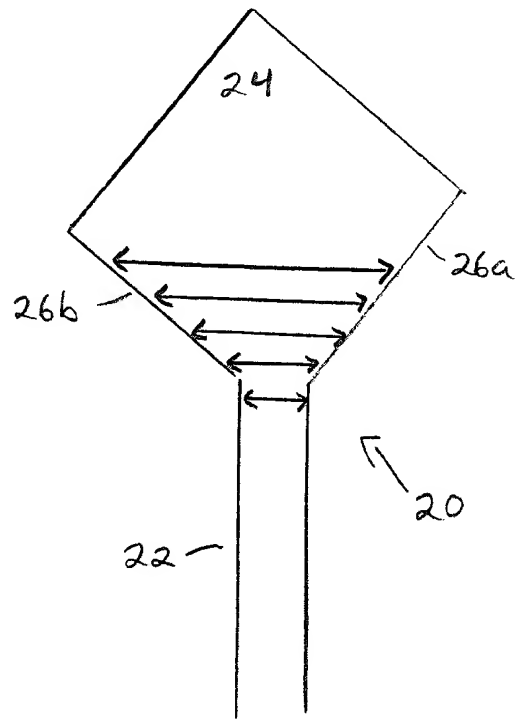


Figure 5

FIG. 7 is a block diagram of a memory system.

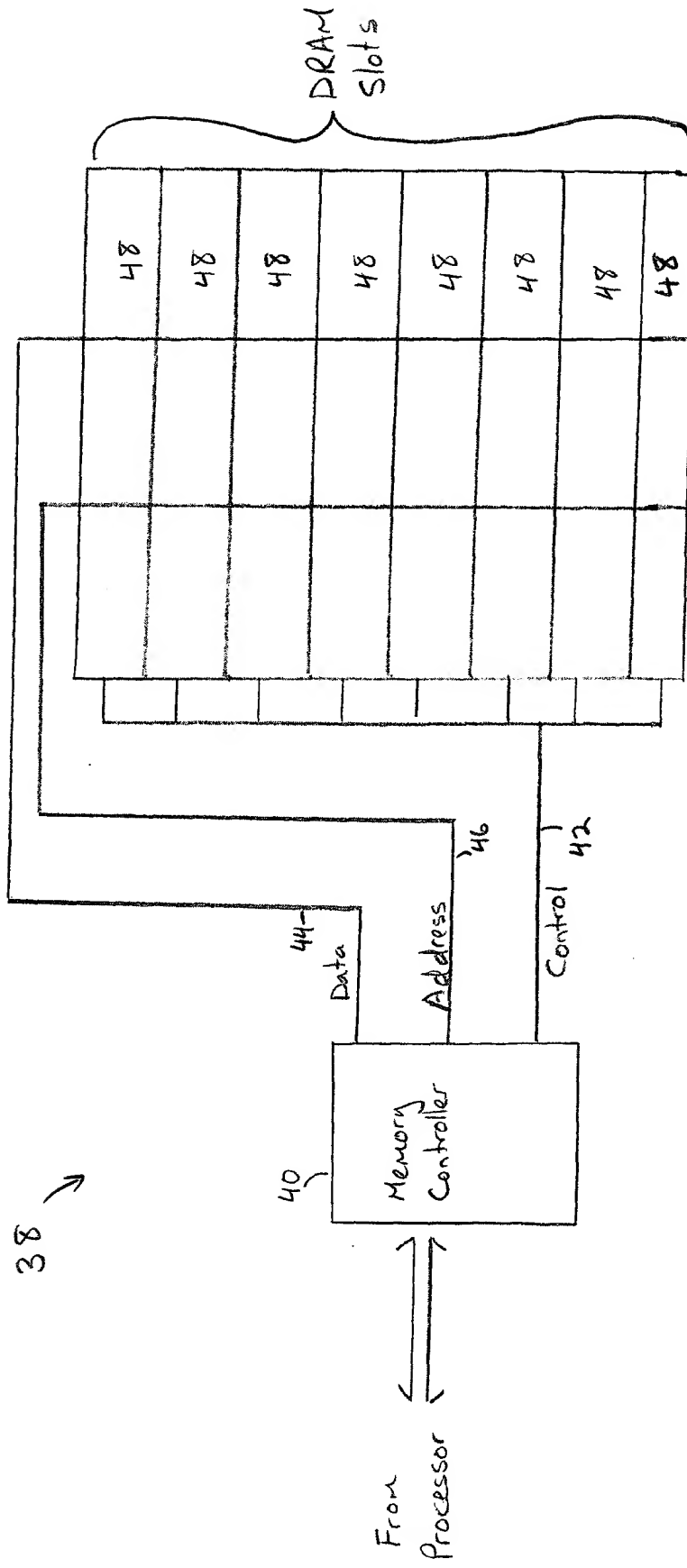


Figure 7

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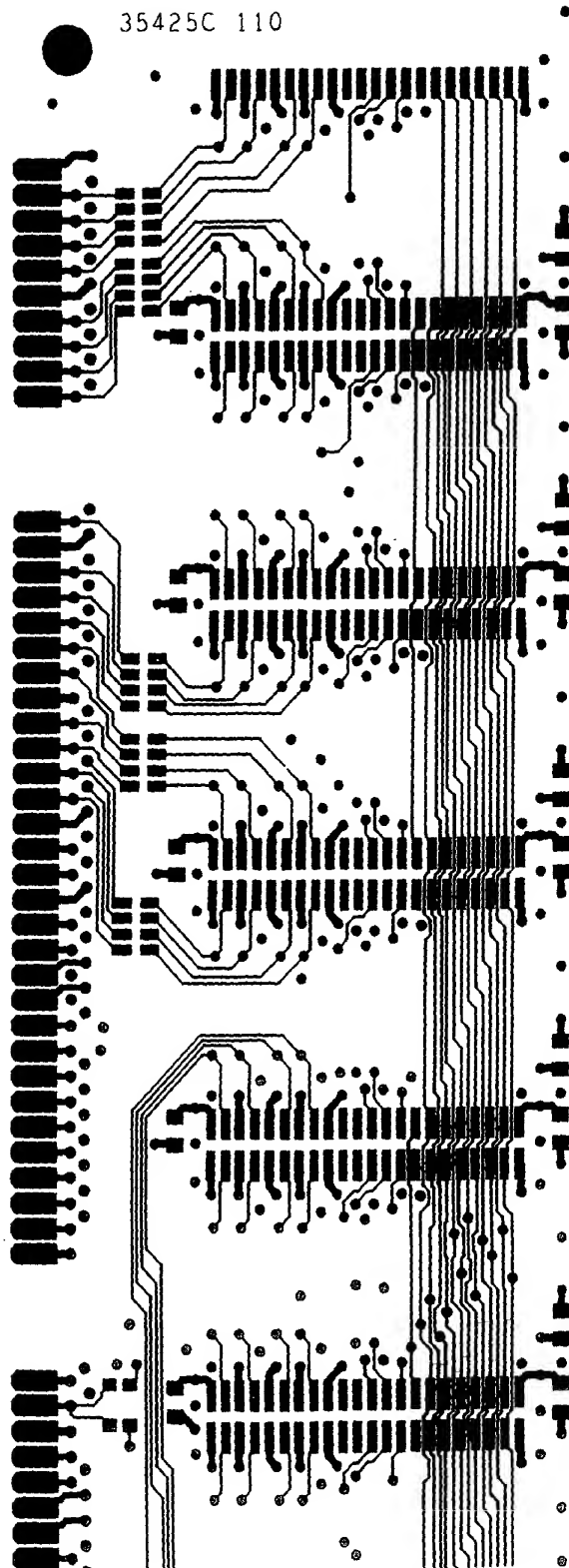


FIG. 8